



**RESPONSE UNDER 37 C.F.R. 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP: 3712**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	Confirmation No.: 3133
Kenji SUZUKI	Art Unit: 2811
Serial No.: 10/058,005	Examiner: Im, Junghwa M
Filed: January 29, 2002	Docket No.: 100353-00096
For:	SEMICONDUCTOR INTEGRATED CIRCUIT CONFIGURED TO SUPPLY SUFFICIENT INTERNAL CURRENT

AMENDMENT AFTER FINAL REJECTION UNDER 37 C.F.R. 1.116

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: November 3, 2003

Sir:

Responsive to the Office Action dated July 3, 2003, the period for response being extended from October 3, 2003, until November 3, 2003, by the attached Petition for Extension of Time, entry of the following amendment is respectfully requested.

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